



Docket No.: 241677US2

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COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313

RE: Application Serial No.: 10/642,222

Applicants: Kenji YOSHIDA, et al.

Filing Date: August 18, 2003

For: METHOD OF ACCELERATING TEST OF  
SEMICONDUCTOR DEVICE

Group Art Unit: 2812

Examiner: Stevenson, Andre C.

SIR:

Attached hereto for filing are the following papers:

**Election**

Our credit card payment form in the amount of \$0.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

KENJI YOSHIDA, ET AL.

: EXAMINER: STEVENSON, A. C.

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: GROUP ART UNIT: 2812

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ELECTION

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SIR:

In response to the restriction requirement dated February 27, 2006, Applicants elect without traverse Group I, Claims 1-10 drawn to a test method, classified in class 438, subclass 14 for further examination on the merits. Applicants reserve the right to file one or more divisional applications directed to the non-elected invention.

Accordingly, examination on the merits of Claims 1-10 is believed to be in order and an early and favorable action is respectfully requested.

Respectfully submitted,

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